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APPLICANTS Michitaka Kameyama, Sendai-shi, JAPAN; Takahiro Hanyu, Sendai-shi, JAPAN; Hiromitsu Kimura, Ukyo-ku, JAPAN; Yoshikazu Fujimori, Ukyo-ku, JAPAN; Takashi Nakamura, Ukyo-ku, JAPAN; Hidemi Takasu, Ukyo-ku, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/00568 01/22/2003				
** FOREIGN APPLICATIONS *****				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <u>Examiner's Signature</u> <u>Initials</u>		STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 14
INDEPENDENT CLAIMS 5				
ADDRESS 000026021				
TITLE Logical operation circuit and logical operation method				
FILING FEE RECEIVED 1382	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	